



ABSTRACT / ZUSAMMENFASSUNG / ABREGE

04251032.1

An error flag generation apparatus and method for error correction, wherein the apparatus includes: a frame-sync error memory (130) which stores frame-sync error information for each data block; a BIS (Burst Indicator Subcode) error flag memory (160) which stores a BIS error flag for each data block; and an error flag generator (150), which generates an error flag indicating error existence/absence for ECC (Error-Correction Coding) data with reference to the frame-sync error information stored in the frame-sync error memory (130) and the BIS error flag stored in the BIS error flag memory (160). The error correction code decoder (140) receives an error flag generated by the error flag generator (150) and performs erasure correction for the symbol signal stored in the memory (120) for each data block. The error flag generation apparatus and method for error correction can be easily implemented with improved error-correction performance and be advantageous in cost.